

symmetrically filter (3) having a symmetrical input (5) and a symmetrical output (6).

6. (Amended) The filter (3) according to claim 1, characterised in that two or more of the FET capacitors (M1-32; M'1-32) are connected in series.

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end
7. (Amended) The filter (3) according to claim 1, characterised in that the FET capacitors (M1-32; M'1-32) are metal oxide semiconductor (MOSFET) capacitors (M1-32; M'1-32).

8. (Amended) A transmitter, receiver, or transceiver having a filter (3) according to claim 1, which filter (3) is provided with field effect (FET) capacitors (M1-32; M'1-32) arranged for controlling their respective capacity values, each such FET capacitor (M1-32; M'1-32) having a source (S) and a drain (D), characterised in that the source (S) and the drain (D) of each FET capacitor (M1-32; M'1-32) are coupled to one another.

REMARKS

The foregoing Preliminary Amendment to claims 3-8 was made solely to avoid filing the claims in the multiple dependant form so as to avoid the additional filing fee.

The claims were not amended in order to address issues of patentability and Applicant respectfully reserves all rights she may have under the Doctrine of Equivalents. Applicant furthermore reserves her right to reintroduce